Abstract

A pipeline-based matching scheduling approach for input-buffered switches relaxes the timing constraint for
5   arbitration with matching schemes, such as CRRD and CMSD. In the new approach, arbitration may operate in a pipelined manner. Each sub-scheduler is allowed to take more than one time slot for its matching. Every time slot, one of them provides a matching result(s). The sub-scheduler can
10   use a matching scheme such as CRRD and CMSD.